3A8-GT

DIODE-TRIODE-R-F AMPLIFIER PENTODE

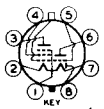
Filament°	Coated		
Filament Arrangement	*Series**	*Parallel***	
Voltage	2.8	1.4	d-c volts
Current	0.05	0.1	amp.

Direct Interelectrode Capacitances:■

Pentode Unit -	Grid to Plate	0.012 max.	μμf
	Input	3.0	μμf
	Output	10	μμf
Triode Unit -	Grid to Plate (approx.)	2.0	μμf
	Grid to Filament (approx.)	2.6	μμf
	Plate to Filament (approx.)	4.2	μμf

Maximum Overall Length	3-7/16"
Maximum Seated Height	2-7/8"
Maximum Diameter	1-5/16"
Bulb	T-9
Cap	Skirted Miniature
Base	Intermed.Sh. Octal 8-Pin

Pin 1 { Fil.Midtap,Supp'r. / Internal Shield
Pin 2 - Filament
Pin 3 - Pentode Plate
Pin 4 - Pentode Screen
Pin 5 - Triode Grid
Pin 6 - Triode Plate
Pin 7 - Filament-
Pin 8 - Diode Plate
Cap - Pentode Grid



BOTTOM VIEW (8AS)

Mounting Position: Any

TRIODE UNIT

Plate Voltage	110 max.	volts
Typical Operation and Characteristics - Class A_1 Amplifier;		
Plate Voltage	90	volts
Grid Voltage °°	0	volts
Amplification Factor	65	
Plate Resistance	0.2 approx.	megohm
Transconductance	325	μmhos
Plate Current	0.2	ma.

PENTODE UNIT

Plate Voltage	110 max.	volts
Screen Voltage	110 max.	volts
Typical Operation and Characteristics - Class A_1 Amplifier:		
Plate Voltage	90	volts
Screen Voltage	90	volts
Grid Voltage °°	0	volts
Plate Resistance	0.8 approx.	megohm
Transconductance	750	μmhos
Plate Current	1.5	ma.
Screen Current	0.5	ma.

DIODE UNIT

The diode plate is located at the negative end of the filament, and is independent of the triode unit and of the pentode unit except for the common filament.

* Filament voltage applied across the two sections in series between pins #2 and #7.

** Filament voltage applied across the two sections in parallel between pin #1 and pins #2 and #7 connected together.

° The filament is designed so that the two sections may be operated satisfactorily with parallel arrangement when connected directly across a 1.5-volt dry battery, or with series arrangement when connected directly across two 1.5-volt dry batteries in series.

■ With close-fitting shield connected to negative filament terminal.

°° Grid voltage for parallel filament arrangement is referred for both triode unit and pentode unit to pins #2 and #7 connected together. For series filament arrangement, grid voltage for triode unit is referred to pin #7 and for pentode unit to pin #1.

←Indicates a change.